

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 10/001,280 EDWARDS ET AL.	
		Examiner	Art Unit	Page 1 of 1 Thomas H. Stevens 2123

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,792,595	09-2004	Storistenau et al.	717/110
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
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	S					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chang et al., "An Efficient Thread Architecture for a Distributed Shared Memory on Symmetric Multiprocessor Clusters" 1998 IEEE pg.816-822.
	V	Srivastava et al., "Vulcan-Binary Transformation in a Distributed Environment" Microsoft Research Technical Report MSR-TR-2001-50. pg.1-12.
	W	Anderson et al., "The Performance Implications of Thread Management Alternatives for Shared-Memory Multiprocessors" IEEE 1989 pg. 1631-1644.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.